

### **REMARKS**

This is a full and timely response to the outstanding non-final Office Action of October 1, 2009. Applicant has noted with great appreciation that the Examiner acknowledged receipt of all certified copies of the priority documents in connection with the present application and that all submitted papers have been placed of record in the file.

#### **Present Status of the Application**

Claim 2 is objected to under 37 CFR 1.75(c) as being of improper dependent form for failing to further limit the subject matter of a previous claim. Claims 1-3, 6-8 and 11 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kang et al. (United State Publication 2002/0063666 A1; "Kang" hereinafter) in view of the instant Application's Admitted Prior Art (AAPA).

Applicant has amended the claim 1 and canceled claims 2 and 6. The amendments are supported by Fig. 6 and paragraph [0034] of the Applicant's disclosure, and therefore no new matter is introduced by entering the proposed amendments.

#### **Discussion of Office Objections**

*Claim 2 is objected to under 37 CFR 1.75(c) as being of improper dependent form for failing to further limit the subject matter of a previous claim.*

In response thereto, Applicant has canceled claim 2 and the objection should be

withdrawn.

**Discussion of Office Rejections under 35 U.S.C. Section 103(a)**

*Claims 1-3, 6-8 and 11 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kang in view of the instant Application's Admitted Prior Art (AAPA).*

Applicants respectfully traverse the rejections for at least the reasons given below.

The amended independent claim 1 recites the features as follows:

“A color management structure for a panel display, comprising:

a display array unit;

a plurality of gate drivers;

a plurality of source drivers, said plurality of gate drivers and said plurality of source drivers driving said display array unit to display an image; and

a timing sequence control unit, said timing sequence control unit outputting a plurality of signals to said plurality of gate drivers and said plurality of source drivers to drive said display array unit, said timing sequence control unit outputting a clock signal and a digital color management data to said plurality of source drivers, said timing sequence control unit comprising:

a timing controller receiving a system input and providing said clock signal; and

a color management control block, coupled to said timing controller, outputting said digital color management data and said clock signal to said plurality of source drivers, said digital color management data being adjustable;\_\_

**each of said plurality of source drivers comprising:**

**a source drive circuit to drive said display array unit; and**

**a programmable data interface receiving said digital color management data and said clock signal to parallel output a plurality of color voltage level signals to said source drive circuit.” (Emphasis Added).**

The Office action holds that Kang discloses all of the limitations in the previously presented claim 1 in Fig.9, Fig.14 and Fig. 15 in view of AAPA, and the Office also holds that Kang discloses all of the limitations of the original claim 6 in Figs. 9 and 15. Applicant does not agree. Applicant indicates that the programmable data interface 92, 93 in Fig. 9 of Kang is disposed outside the source driver 97. That is, Kang discloses only one set of the programmable data interface 92, 93 for providing GAMMA voltages to all of the source drivers.

Please note here, Applicant submits that the amended claim 1 discloses each of said plurality of source drivers comprising a source drive circuit to drive said display array unit and a programmable data interface for receiving said digital color management data and said clock signal to parallel output a plurality of color voltage level signals to said source drive circuit. That is, all the source drivers are programmed through the programmable data interface thereof, and the GAMMA voltages (color voltage level signals) used by all of the source drivers are different.

Paragraph [0034] of Applicant's Specification supports this: "Regarding the design of the source driver 204, the programmable interface 300 and the traditional source driver 122 can be integrated to be the source driver 204 of the present invention as shown in FIG. 6. The programmable interface 300 can also be disposed between the timing sequence control unit 256 and the source driver 122."

Applicant also submits that the panel display disclosed by newly amended claim 1 overcomes the problem of the variations of each of the columns caused by the process of programming different color voltage level signals to each of the source drive circuits.

Accordingly, the programming interfaces are embedded in each of the source drivers separately in the amended claim 1 and the amended claim 1 is different from the disclosure of Kang. Applicant respectfully requests that the 103 rejection of claim 1 over Kang in view of AAPA be withdrawn.

Since claims 3, 7-8 and 11 depend on the allowable claim 1, these dependent claims should also be non-obvious and allowable, and the 103 rejections of claims 3, 7-8, and 11 relying upon the Kang reference in view of AAPA should also be withdrawn.

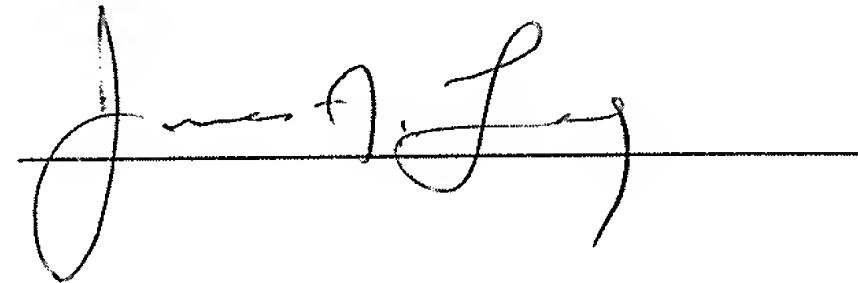
**CONCLUSION**

For at least the foregoing reasons, it is believed that the pending claims 1, 3, 7-8 and 11, the specification and the drawings are in proper condition for allowance. An action to such effect is most respectfully requested. If the Examiner believes that a telephone conference would expedite the examination of the above-identified patent application, the Examiner is invited to call the undersigned.

Date :

12 | 15 | 2009

Respectfully submitted,



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